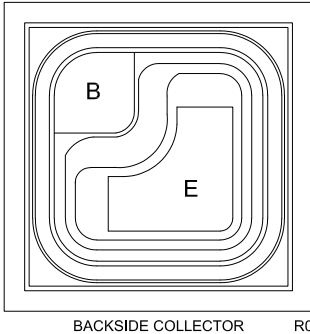


# CP388X-BC847C

## NPN - Low Noise Transistor Die

The CP388X-BC847C is a silicon NPN transistor designed for low noise amplifier applications.



### MECHANICAL SPECIFICATIONS:

Die Size	13 x 13 MILS
Die Thickness	5.9 MILS
Base Bonding Pad Size	3.9 x 3.9 MILS
Emitter Bonding Pad Size	5.4 x 5.4 MILS
Top Side Metalization	Al-Si – 17,000Å
Back Side Metalization	Au – 9,000Å
Scribe Alley Width	1.8 MILS
Wafer Diameter	5 INCHES
Gross Die Per Wafer	102,852

### MAXIMUM RATINGS: (T<sub>A</sub>=25°C)

Collector-Base Voltage	V <sub>CBO</sub>	50	V
Collector-Emitter Voltage	V <sub>CEO</sub>	45	V
Emitter-Base Voltage	V <sub>EBO</sub>	6.0	V
Continuous Collector Current	I <sub>C</sub>	100	mA
Peak Collector Current	I <sub>CM</sub>	200	mA
Peak Base Current	I <sub>BM</sub>	200	mA
Operating and Storage Junction Temperature	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

### SYMBOL

### BC847

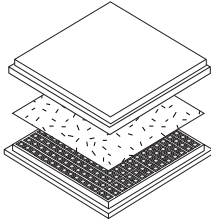
### UNITS

### ELECTRICAL CHARACTERISTICS: (T<sub>A</sub>=25°C unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
I <sub>CBO</sub>	V <sub>CB</sub> =30V			15	nA
I <sub>EBO</sub>	V <sub>EB</sub> =5.0V			100	nA
BV <sub>CBO</sub>	I <sub>C</sub> =10μA	50			V
BV <sub>CEO</sub>	I <sub>C</sub> =10mA	45			V
BV <sub>EBO</sub>	I <sub>E</sub> =10μA	6.0			V
V <sub>CE(SAT)</sub>	I <sub>C</sub> =10mA, I <sub>B</sub> =0.5mA			0.25	V
V <sub>CE(SAT)</sub>	I <sub>C</sub> =100mA, I <sub>B</sub> =5.0mA			0.60	V
V <sub>BE(SAT)</sub>	I <sub>C</sub> =10mA, I <sub>B</sub> =0.5mA		0.70		V
V <sub>BE(SAT)</sub>	I <sub>C</sub> =100mA, I <sub>B</sub> =5.0mA		0.90		V
V <sub>BE(ON)</sub>	I <sub>C</sub> =2.0mA, V <sub>CE</sub> =5.0V	0.58		0.70	V
V <sub>BE(ON)</sub>	I <sub>C</sub> =10mA, V <sub>CE</sub> =5.0V			0.77	V
h <sub>FE</sub>	V <sub>CE</sub> =5.0V, I <sub>C</sub> =2.0mA	420		800	
C <sub>cb</sub>	V <sub>CB</sub> =10V, I <sub>E</sub> =0, f=1.0MHz			4.5	pF

## BARE DIE PACKING OPTIONS

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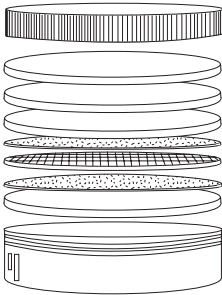


### BARE DIE IN TRAY (WAFFLE) PACK

**CT:** Singulated die in tray (waffle) pack.  
(example: CP211-PART NUMBER-CT)

**CM:** Singulated die in tray (waffle) pack 100% visually inspected as per MIL-STD-750, (method 2072 transistors, method 2073 diodes).  
(example: CP211-PART NUMBER-CM)

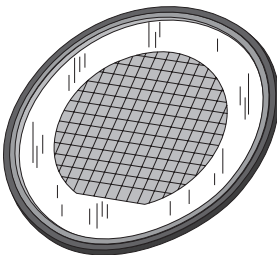
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### UNSAWN WAFER

**WN:** Full wafer, unsawn, 100% tested with reject die inked.  
(example: CP211-PART NUMBER-WN)

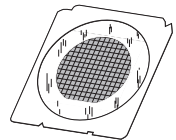
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### SAWN WAFER ON PLASTIC RING

**WR:** Full wafer, sawn and mounted on plastic ring,  
100% tested with reject die inked.  
(example: CP211-PART NUMBER-WR)

Please note: Sawn Wafer on Metal Frame (WS) is possible as a special order. Please contact your Central Sales Representative at 631-435-1110.



Visit the Central website for a complete listing of specifications:  
[www.centrasemi.com/bdspecs](http://www.centrasemi.com/bdspecs)

## OUTSTANDING SUPPORT AND SUPERIOR SERVICES



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### PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options
- Custom bar coding for shipments
- Custom product packing

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### DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2<sup>nd</sup> day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities
- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- Application and design sample kits
- Custom product and package development

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### REQUESTING PRODUCT PLATING

1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

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### CONTACT US

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