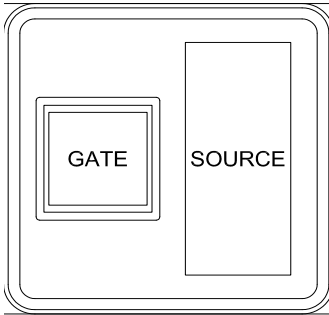


CP324X-2N7002A

N-Channel MOSFET Die

Enhancement-Mode

The CP324X-2N7002A is a silicon N-Channel MOSFET designed for high speed pulsed amplifier and driver applications.



MECHANICAL SPECIFICATIONS:

Die Size	21.65 x 21.65 MILS
Die Thickness	5.9 MILS
Gate Bonding Pad Size	5.5 x 5.5 MILS
Source Bonding Pad Size	5.9 x 13.8MILS
Top Side Metalization	Al – 30,000Å
Back Side Metalization	Au – 12,000Å
Scribe Alley Width	1.97 MILS
Wafer Diameter	5 INCHES
Gross Die Per Wafer	34,700

MAXIMUM RATINGS: (T_A=25°C)

	SYMBOL		UNITS ^{R0}
Drain-Source Voltage	V _{DS}	60	V
Drain-Gate Voltage	V _{DG}	60	V
Gate-Source Voltage	V _{GS}	40	V
Continuous Drain Current	I _D	280	mA
Continuous Source Current (Body Diode)	I _S	280	mA
Maximum Pulsed Drain Current	I _{DM}	1.5	A
Maximum Pulsed Source Current	I _{SM}	1.5	A
Operating and Storage Junction Temperature	T _J , T _{stg}	-65 to +150	°C

ELECTRICAL CHARACTERISTICS: (T_A=25°C unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
I _{GSSF} , I _{GSSR}	V _{GS} =20V, V _{DS} =0		100	nA
I _{DSS}	V _{DS} =60V, V _{GS} =0		1.0	μA
I _{D(ON)}	V _{GS} =10V, V _{DS} =10V	500		mA
BV _{DSS}	V _{GS} =0, I _D =10μA	60		V
V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	1.0	2.5	V
V _{DS(ON)}	V _{GS} =10V, I _D =500mA		1.0	V
V _{DS(ON)}	V _{GS} =5.0V, I _D =50mA		0.15	V
V _{SD}	V _{GS} =0, I _S =11.5mA		1.2	V
r _{DS(ON)}	V _{GS} =10V, I _D =500mA		2.0	Ω
r _{DS(ON)}	V _{GS} =5.0V, I _D =50mA		3.0	Ω
g _{FS}	V _{DS} =10V, I _D =200mA	80		mS
C _{rss}	V _{DS} =25V, V _{GS} =0, f=1.0MHz		5.0	pF
C _{iss}	V _{DS} =25V, V _{GS} =0, f=1.0MHz		50	pF
C _{oss}	V _{DS} =25V, V _{GS} =0, f=1.0MHz		25	pF

CP324X-2N7002A

N-Channel MOSFET Die

Enhancement-Mode

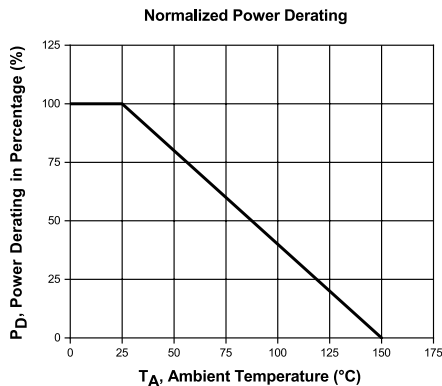
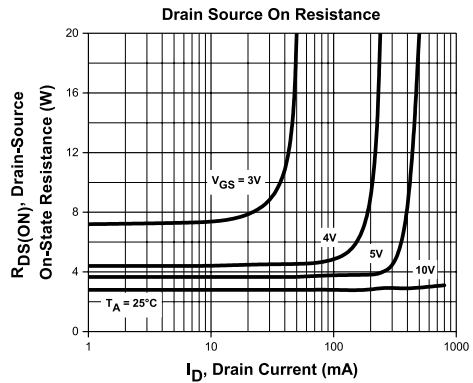
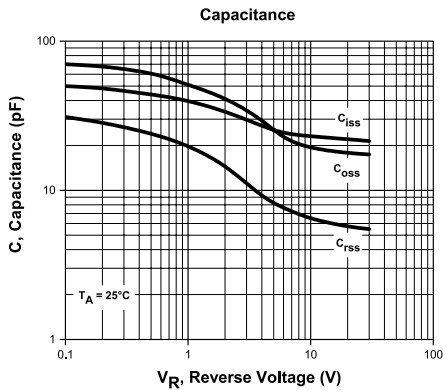
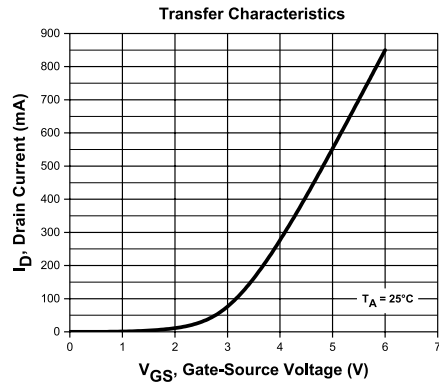
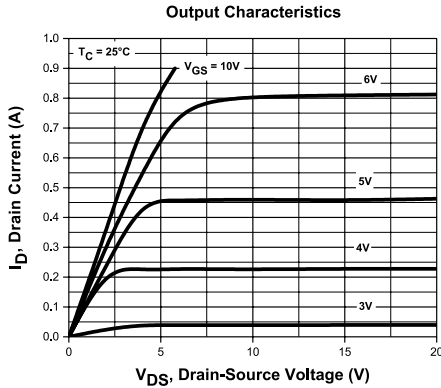


ELECTRICAL CHARACTERISTICS - Continued: ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

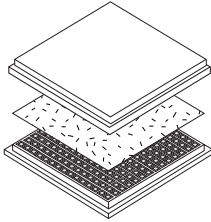
SYMBOL	TEST CONDITIONS	TYP	MAX	UNITS
$Q_{g(\text{tot})}$	$V_{DS}=30\text{V}, V_{GS}=4.5\text{V}, I_D=100\text{mA}$	0.592		nC
Q_{gs}	$V_{DS}=30\text{V}, V_{GS}=4.5\text{V}, I_D=100\text{mA}$	0.196		nC
Q_{gd}	$V_{DS}=30\text{V}, V_{GS}=4.5\text{V}, I_D=100\text{mA}$	0.148		nC
t_{on}	$V_{DD}=30\text{V}, V_{GS}=10\text{V}, I_D=200\text{mA},$ $R_G=25\Omega, R_L=150\Omega$		20	ns
t_{off}			20	ns

CP324X-2N7002A

Typical Electrical Characteristics



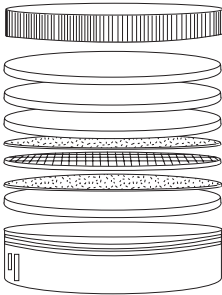
BARE DIE PACKING OPTIONS



BARE DIE IN TRAY (WAFFLE) PACK

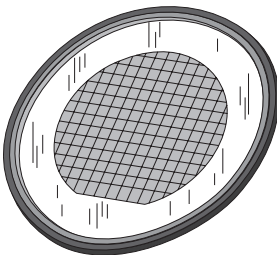
CT: Singulated die in tray (waffle) pack.
(example: CP211-PART NUMBER-CT)

CM: Singulated die in tray (waffle) pack 100% visually inspected as per MIL-STD-750, (method 2072 transistors, method 2073 diodes).
(example: CP211-PART NUMBER-CM)



UNSAWN WAFER

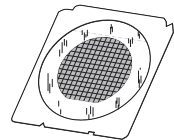
WN: Full wafer, unsawn, 100% tested with reject die inked.
(example: CP211-PART NUMBER-WN)



SAWN WAFER ON PLASTIC RING

WR: Full wafer, sawn and mounted on plastic ring,
100% tested with reject die inked.
(example: CP211-PART NUMBER-WR)

Please note: Sawn Wafer on Metal Frame (WS) is possible as a special order. Please contact your Central Sales Representative at 631-435-1110.



Visit the Central website for a complete listing of specifications:
www.centrasemi.com/bdspecs

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options
- Custom bar coding for shipments
- Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities
- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- Application and design sample kits
- Custom product and package development

REQUESTING PRODUCT PLATING

1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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